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Sheet	1	of	1	Attorney Docket Number	TI-38252

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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TD	A	JUNG, S., et al., "Packaging Consideration for 1 Gbps Si CMOS Optical Driver," IEEE (2000) pp. 882-883.	
TD	B	FAN, J., et al., "Quantifying Decoupling Capacitor Location," IEEE (February 2000) pp. 761-766.	
	C	PEIFFER, J., et al., "Electrical Performance Advantages of Ultra-Thin Dielectric Materials Used for Power-Ground Cores in High-Speed, Multilayer Printed Circuit Boards," Manuscript of Paper for IPC Expo 2003 (March 25-27, 2003) pp. 1-13.	

Examiner Signature	<i>Mundo</i>	Date Considered	2/21/06
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